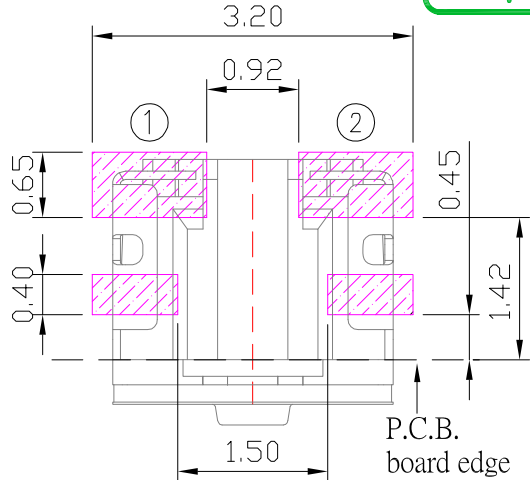
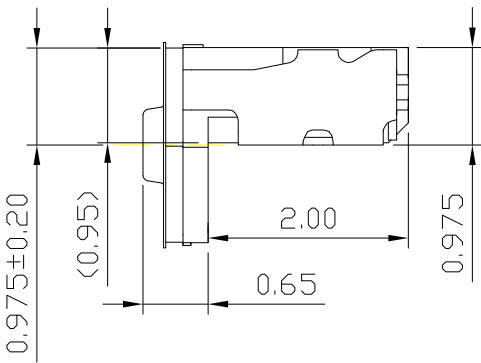
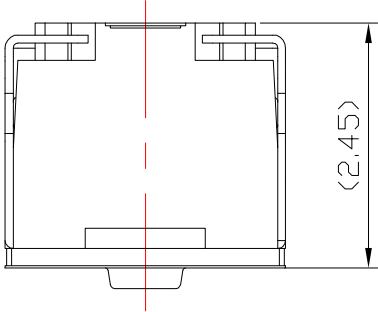
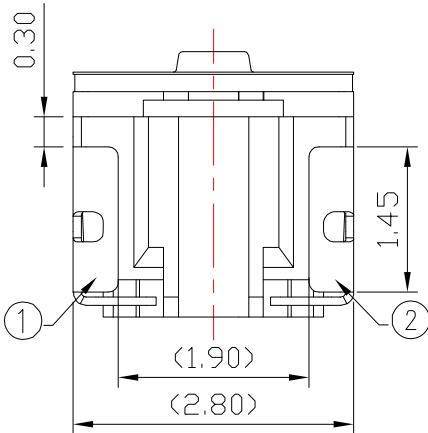
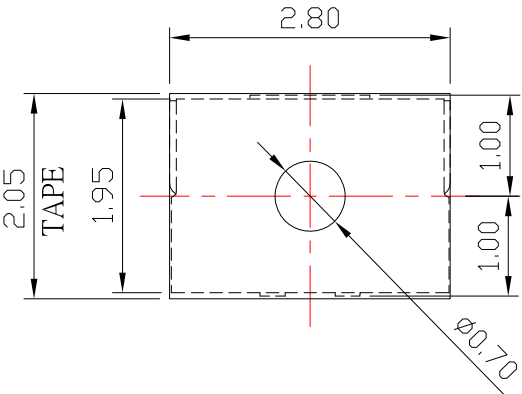


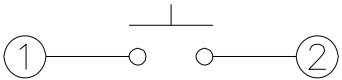


P.C.B LAYOUT



SPECIFICATIONS

Rating	20mA , 15V DC
Insulation Resistance	50MΩ Min.
Dielectric Withstanding Voltage	AC 250 V
Contact Resistance	500mΩ Max.
Travel	0.13± 0.10mm
Operating Force	160 ± 50gf
Operating Life(cycles)	300,000



CIRCUIT DIAGRAM

E	TAPE	1	PI	
D	STEM	1	PI(Red)	
C	CONTACT	2	SUS	Ag plated
B	BASE	1	PA9T(Black)	
A	TERMINAL	1	PHOSPHOR BRONZE	Ag plated
NO.	DESCRIPTION	QTY	MATERIAL	PLATING



東莞市高迪電子有限公司
DONG GUAN GAO DI ELECTRONICS CO., LTD

1	新增PCB LAYOUT外型樣式	Jack	080725
ITEM NO.	DESCRIPTION	DRAWN	DATE

TOLERANCE UNLESS OTHERWISE STATED :	Up to 5 ±0.2 Above 5 ~ 15 ±0.3 Above 15 ~ 30 ±0.4 Above 30 ~ 50 ±0.5 Angle ±0.3°	3RD. ANGEL'S		UNITS	MM
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DRAWN BY:	DATE	MAT'L	TITLE	SWITCH
Jack Lu	08/07/25	FINISH	MODLE	TACT SWITCH
CHECKED BY:	DATE	SCALE	DWG NO.	TS-A101-2-S410
Jacky Chen	08/07/25	1 : 1	PART NO.	TS-A101-2-S410
APPROVED BY:	DATE	SHEET NO.	1 of 1	
Tony Kao	08/07/25			